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Patent Application

Docket No. 06940/0032

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

GROUP 2300

In re application of:

JAMES E. BOWLES ET AL.

Serial No. 07/917,503

Filing Date: July 21, 1992

For: INTERRUPT MASK DISABLE
CIRCUIT AND METHOD§
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Group No.: 2308

Examiner: RAY, G.

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Debbie Sherwood
Debbie Sherwood

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Dear Sir:

TRANSMITTAL LETTER

Transmitted herewith for filing, please find an Information Disclosure Statement, a PTO-1449, and copies of the cited references.

— Also enclosed is a check in the amount of \$200.00 to pay the fee set forth in 37 C.F.R. § 1.17(p).

XX No fee is believed to be required for this filing. The undersigned hereby certifies that each item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the statement. Additionally, to the best of the undersigned's knowledge, neither a final action nor a notice of allowance has heretofore been mailed with respect to this patent application.

XX The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. 10-0774. This sheet is attached in triplicate.

XX Any additional filing fees required under 37 CFR §1.16 including fees for presentation of extra claims.

XX Any additional patent application processing fees under 37 CFR §1.17 and under 37 CFR §1.20(d).


XX The Commissioner is hereby authorized to charge payment of the following fees during the pendency of this application or credit any overpayment to Deposit Account No. 10-0774. This sheet is attached in triplicate.

XX Any patent application processing fees under 37 CFR §1.17 and under 37 CFR §1.20(d).

— The issue fee set in 37 CFR §1.18 at or before mailing of the Notice of Allowance, pursuant to 37 CFR §1.311(b).

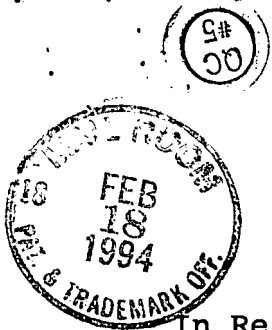
XX Any filing fees under 37 CFR §1.16 including fees for presentation of extra claims.

Date: 2/17/94



Roger L. Maxwell
Registration No. 31,855

JOHNSON & GIBBS, P.C.
100 Founders Square
900 Jackson Street
Dallas, Texas 75202-4499
(214) 977-9662



CP2308
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In Re Application of:

JAMES E. BOWLES ET AL.

Serial No.: 07/917,503

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For: INTERRUPT MASK DISABLE
CIRCUIT AND METHOD

Honorable Commissioner
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Washington, D.C. 20231

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Art Unit: 2308

Examiner: RAY, G.
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Date of Deposit Feb. 18, 1994

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Debbie Sherwood
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MAR 03 1994
GROUP 2308

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In accordance with the Applicants' duty of disclosure under 37 C.F.R. § 1.56, the Applicants hereby submit the following Information Disclosure Statement under 37 C.F.R. § 1.97 and § 1.98.

The Applicants call the Examiner's attention to the following patents, publications or other information:

Document AA. United States Patent, Number 4,010,448, issued on March 1, 1977 to Bennett et al. This patent relates to interrupt circuitry for microprocessor chips.

Document AB. United States Patent, Number 5,095,526, issued on March 10, 1992 to Allen J. Baum. This patent relates to an interrupt handling routine for a microprocessor.

Document AC. Japan Patent, Publication number JP4-135246, Publication date May 8, 1992, applied for by MITSUBISHI ELECTRIC CORP.; Patent Abstracts of Japan, Abstract volume 016404,

Abstract Publication Date August 26, 1992. The abstract relates to a system wherein a period during which an interruption mask is produced in a user mode is counted and wherein a mask unable interruption is forcibly produced when a time-out state is detected.

The submission of any document herewith is not intended as an admission that such document constitutes prior art against the claims of the present application. Applicants do not waive any rights to take any action which would be appropriate to antedate or otherwise remove as a competent reference any document which is determined to be a prima facie prior art reference against the claims of the present invention.

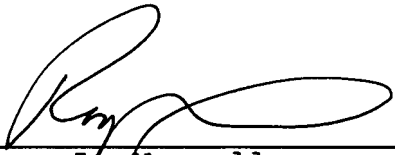
Copies of each of the references listed on the attached Form PTO-1449 are enclosed herewith. Certain of the references may contain markings, underlinings or other notations. These markings are not intended and should not be construed as drawing the Examiner's attention either to selected parts or away from other parts of the references obtained by Applicants, nor should they necessarily be construed as having been made thereon during the study of the references by Applicants and/or their attorneys.

The Applicants respectfully request that the Examiner consider and cite the above-listed patents, publications, and other information. A copy of each cited document, together with a completed copy of PTO-Form 1449 is included for that purpose.

No fee is believed to be required for this filing. The undersigned hereby certifies that each item of information contained in this Information Disclosure Statement was cited in a

communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. Additionally, to the best of the undersigned's knowledge, neither a final action nor a notice of allowance has heretofore been mailed with respect to this patent application.

Respectfully submitted,



Roger I. Maxwell
Reg. No. 31,855

Dated: _____

2/17/94

JOHNSON & GIBBS, P.C.
100 Founders Square
900 Jackson Street
Dallas, Texas 75202-4499
(214) 977-9662